

Statement of [memory] volatility, for SharkTap devices 10/100/1G, USB and BYP models.

- 1) The ethernet PHY chips and the FPGA contain enough RAM memory to temporarily store and forward several packets of data. This data is lost when power is removed. *There is no non-volatile storage of ethernet traffic.*
- 2) Rev J and later versions of the SharkTap could fail in such a way that one or more of the ports will not pass and/or receive data. The SharkTaps cannot alter encapsulated data under any functional or known failure modes. Earlier SharkTaps, based on switch chips, might fail in a way that would allow the PC connected to the TAP to inject data into the Network ports, and/or slow Network traffic, but this is not the case with Rev J and later.

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rev D

